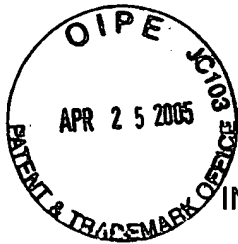


2823
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Attorney Docket No. 0756-2344

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 09/916,484

Filed: July 30, 2001

For: ELECTRIC DEVICE, MATRIX
DEVICE, ELECTRO-OPTICAL
DISPLAY DEVICE, AND
SEMICONDUCTOR MEMORY
HAVING THIN-FILM TRANSISTORS

) Group Art Unit: 2823

) Examiner: T. Pham

) CERTIFICATE OF MAILING

) I hereby certify that this correspondence is
) being deposited with the United States Postal
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) Alexandria, VA 22313-1450, on

4-21-05

Rose J. Tipton

REQUEST FOR CORRECTED PTO 1449 FORM

Honorable Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

It has come to the attention of Applicant that the PTO 1449 Form accompanying the Information Disclosure Statement filed July 30, 2001 requires correction.

On page 1, the prefix of JP 0 194351 is "01" and not "0" and the prefix of JP 0 154383 is "03" and not "0."

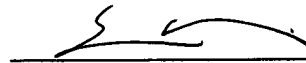
On page 2, the patentee of U.S. Patent No. 4,709,993 is "Matsuyama et al." and not "Kuribayashi et al.;" the prefix of JP 0 011329 is "55" and not "0;" the prefix of JP 0 113971 is "60" and not "0;" the prefix of JP 0 269358 is "62" and not "0;" and the prefix of JP 0 282464 is "62" and not "0."

On page 3, the patentee of U.S. Patent No. 5,165,073 is "Shaklee et al." and not "Hiroki et al.;" the prefix of JP 0 099778 is "57" and not "0;" the prefix of JP 0 159 544 is "59" and not "0;" the prefix of JP 0 159826 is "63" and not "0;" the prefix of JP 0 089030 is "02" and not "0;" the prefix of JP 0 096636 is "63" and not "0;" the prefix of JP 0 144297 is "01" and not "0;" and the prefix of JP 0 081 621 is "59" and not "0."

On page 4, the JP 5 881972 is incorrect and should be corrected to **JP 56-081972**.

Therefore, it is respectfully requested that the Examiner line through the erroneous listing on the PTO 1449 filed July 30, 2001, and evidence his consideration of by initialing the attached PTO 1449 Form.

Respectfully submitted,



Eric J. Robinson
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PTO/SB/08A (08-00)